

# REFRESHING OF MULTI-PORT MEMORY IN INTEGRATED CIRCUITS

## Abstract of Disclosure

A dual port memory module comprising a contention circuit for refresh in order to detect a conflict between an externally requested access and a refresh operation is described. The refresh operation is allocated to the port that is not externally accessed. When accesses through both ports are requested, a wait cycle for one of the access requests is inserted until the refresh is terminated.

## Figures